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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Fabrice BLISSON and Gilles DEBEURME
Serial No: 10/774,896
Filed: February 9, 2004
For: CIRCUIT WITH AN INTEGRATED VOLTAGE REGULATOR AND ITS
MANUFACTURING PROCESS

Examiner: Kevin V. Quinto
Art Unit: 2826

Confirmation No.: 8769

MAIL STOP RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir/Madam:

Prior to examination, please amend this application as follows:

A complete Listing of the Claims in this application begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

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Application or Docket Number
10/774896

AMENDMENT C	CLAIMS REMAINING AT FILING DATE	HIGHEST NUMBER OF DEPENDENT CLAIMS	PREVIOUS FILING
1. CLAIMS REMAINING AT FILING DATE	1	1	1
2. CLAIMS REMAINING AT FILING DATE	1	1	1
TOTAL PREVIOUS FILING DATE DEPENDENT CLAIMS (3) CUMULATIVE			

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